

Fig. 1

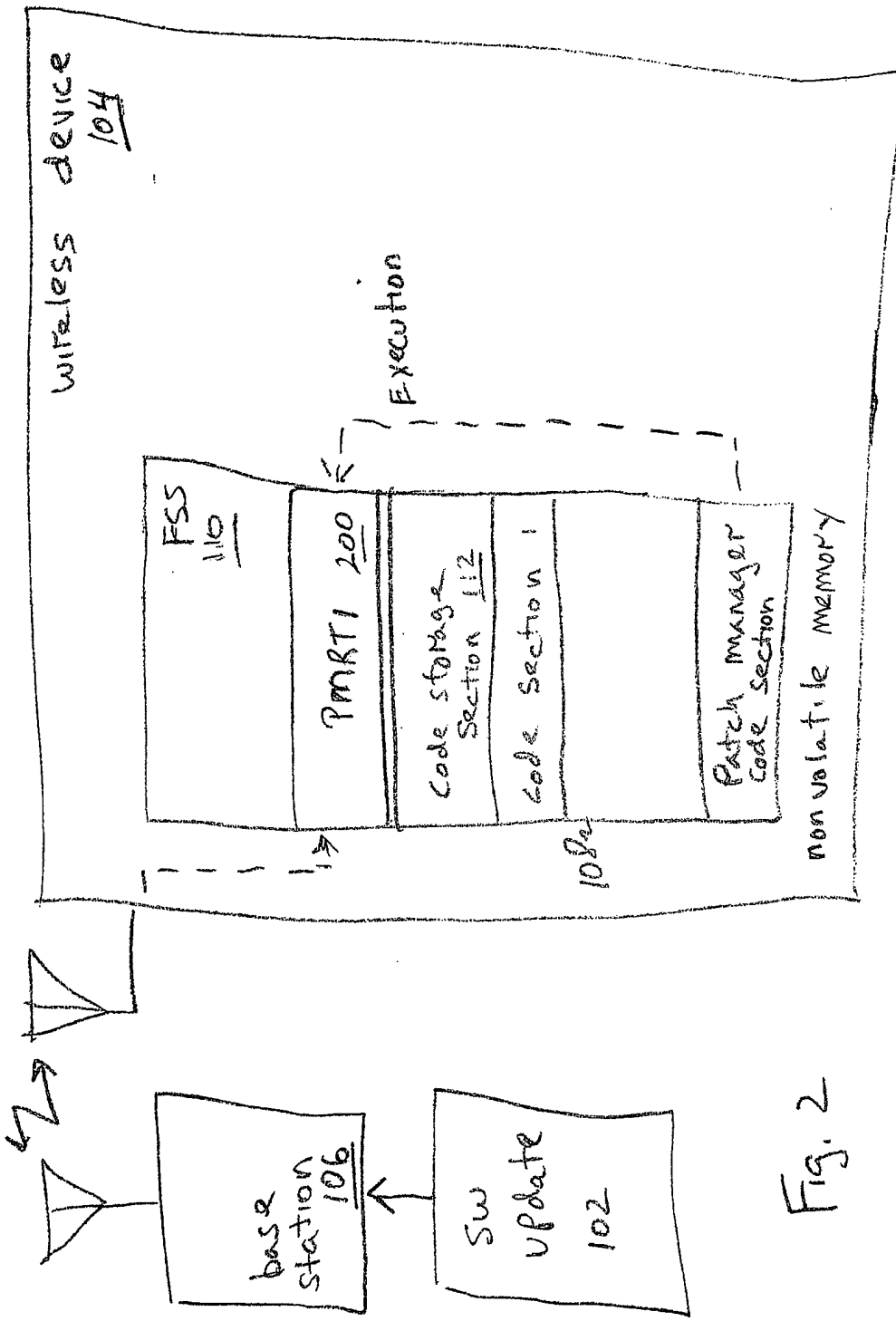


Fig. 2

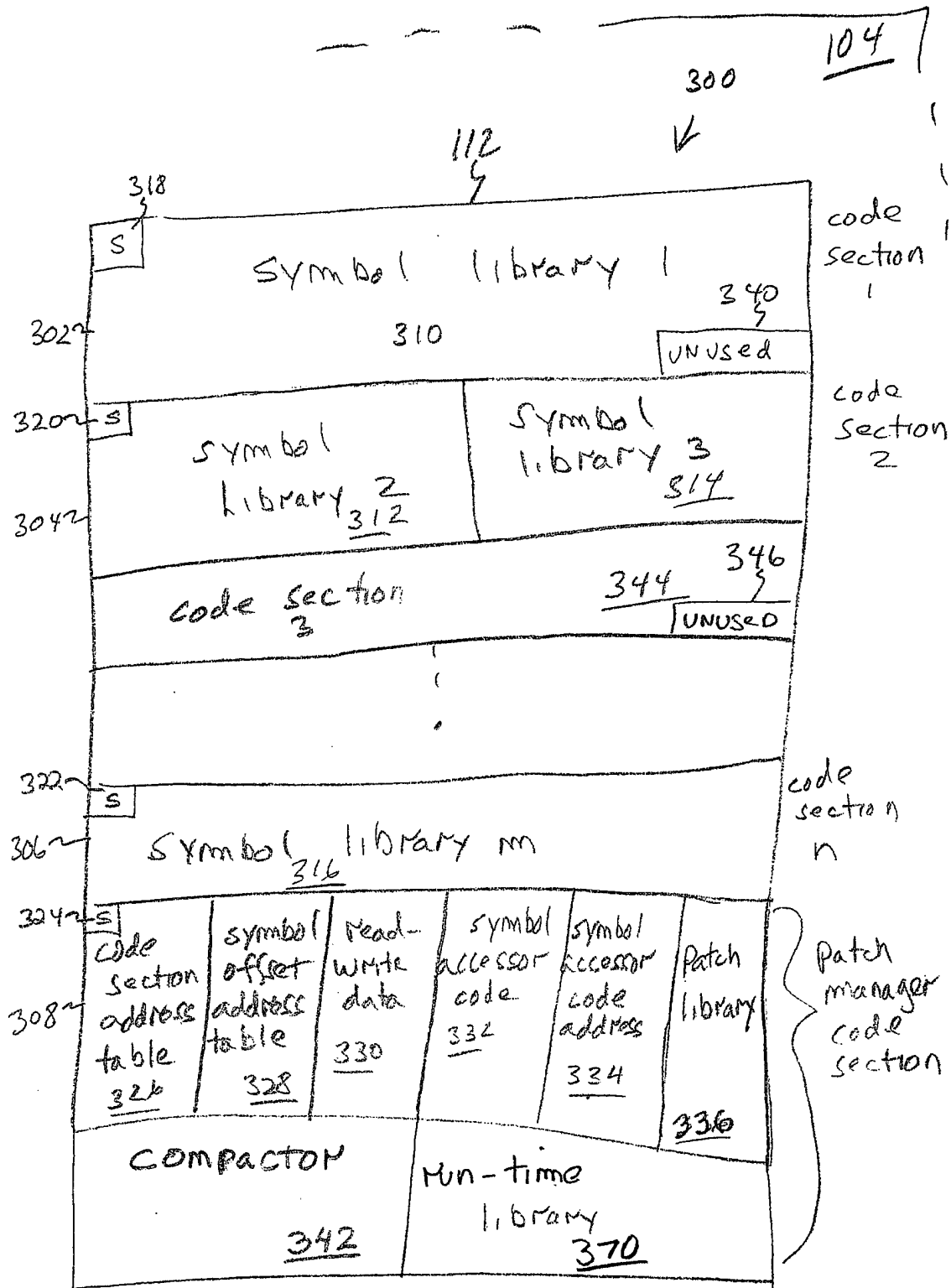


Fig. 3

100180-111/2660

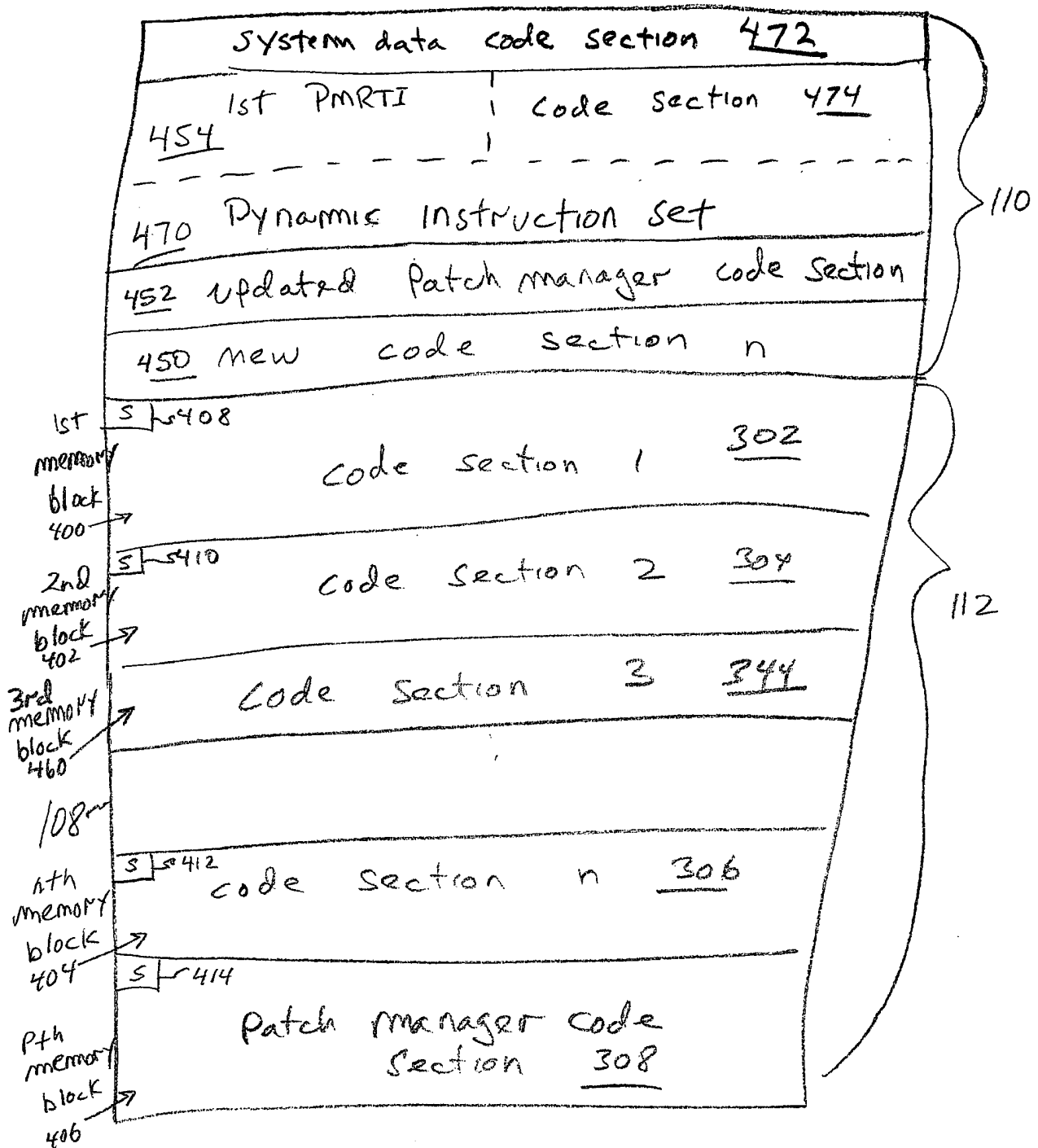


Fig. 4

3262

code Section address table	
Identifiers	Addresses
CS-1	start address 1 (00100)
CS-2	start address 2 (00200)
⋮	
CS-n	start address n (00700)
Pm	start address P (01000)

Fig. 5

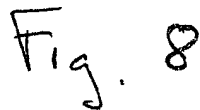
Symbol offset address table		
Symbol ID	code section ID	off set
X-1	CS-1	03
Y-1	CS-1	15
P-1	CS-2	11
Q-1	CS-2	33
AA-3	CS-2	47
⋮		

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Fig. 7

1st instruction	<u>800</u>
2nd instruction	<u>802</u>
jth instruction	<u>804</u>

470



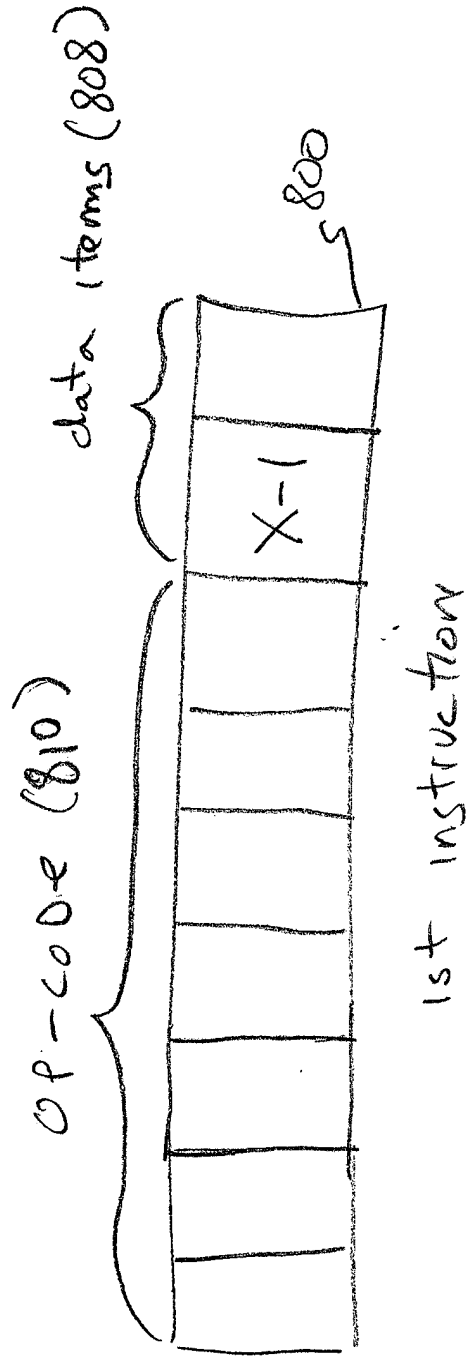
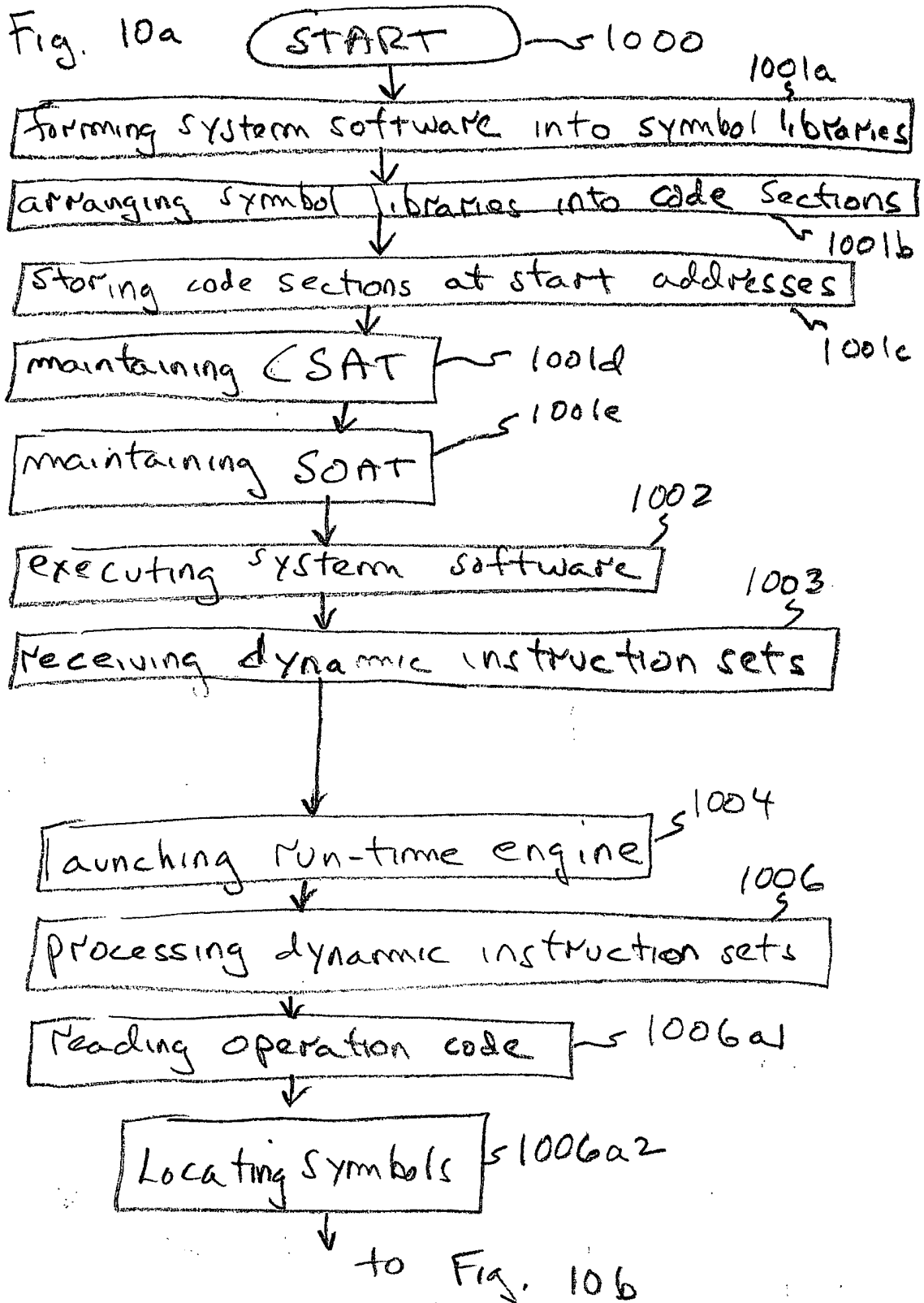


Fig. 9



09927331-031001

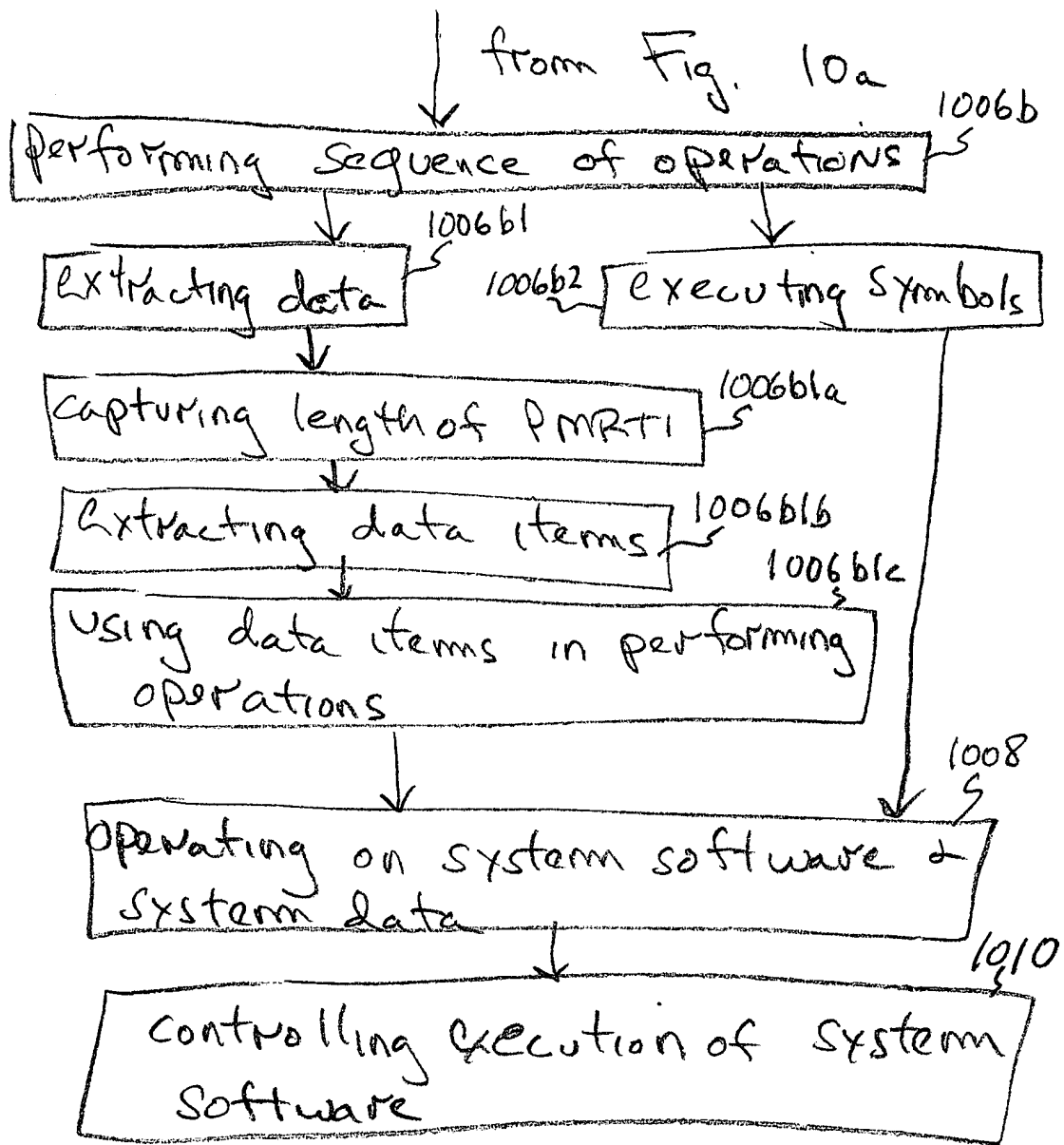
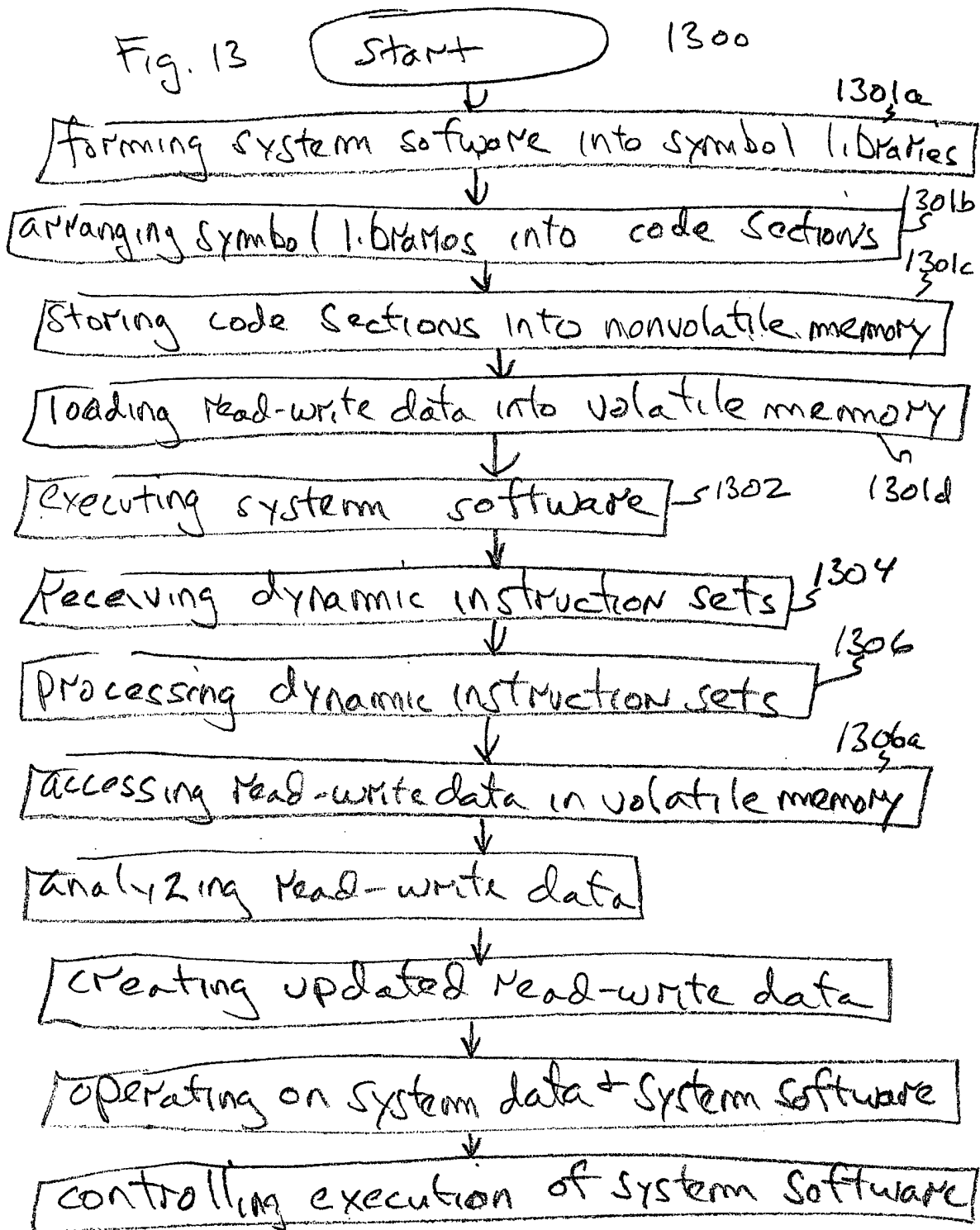
[illegible]

Fig. 106

Fig. 13



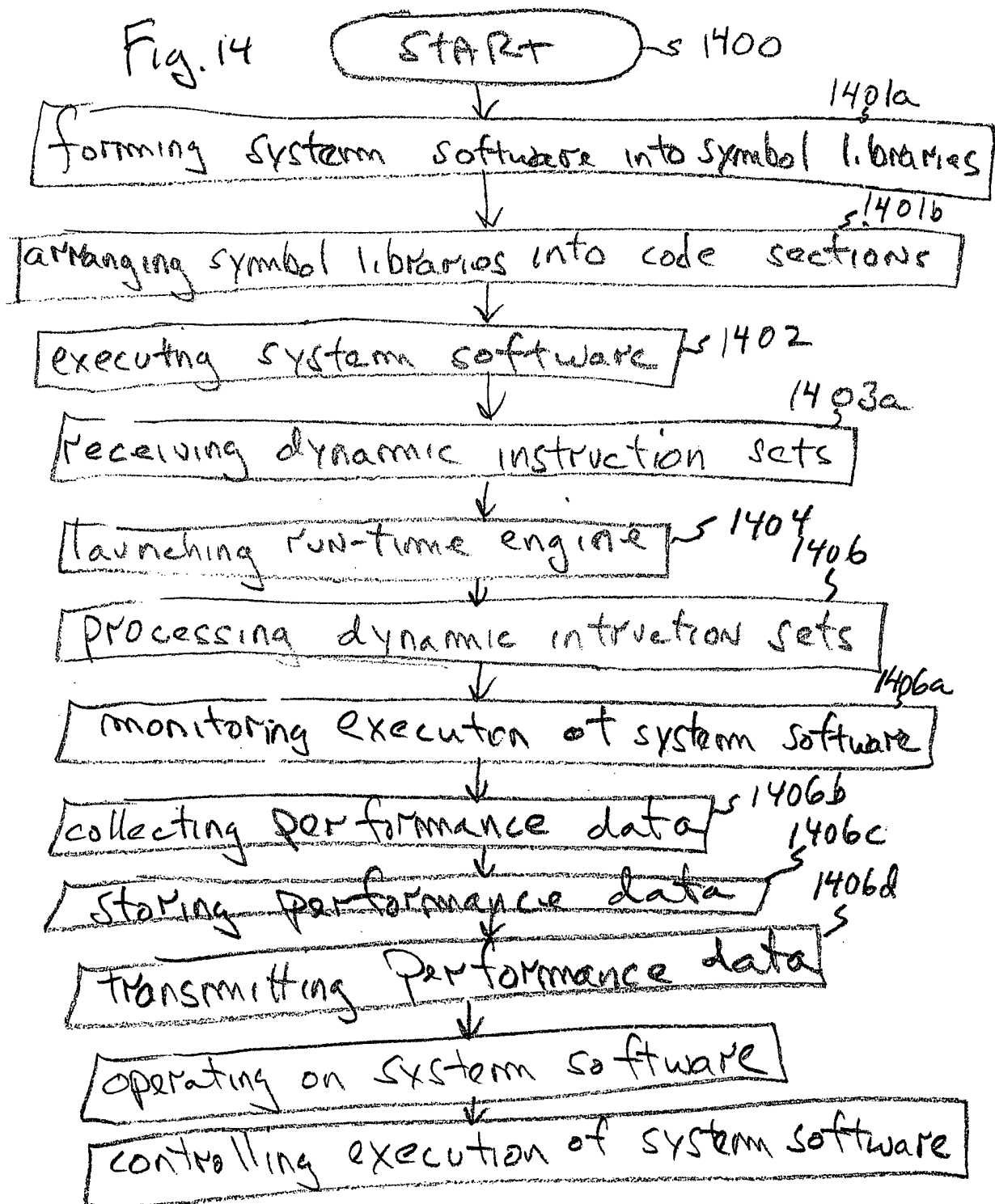
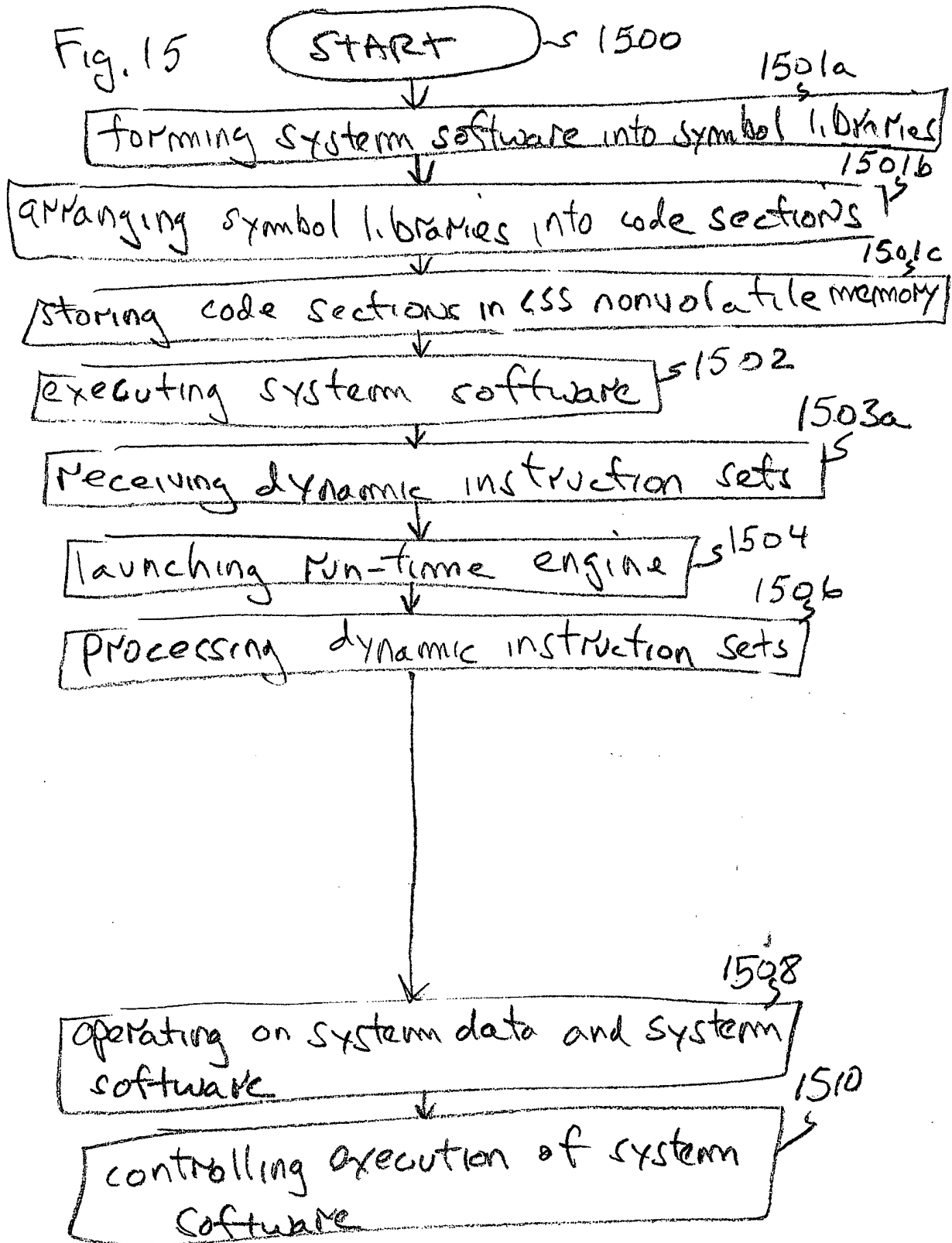
[illegible]

Fig. 15



09927131.081001